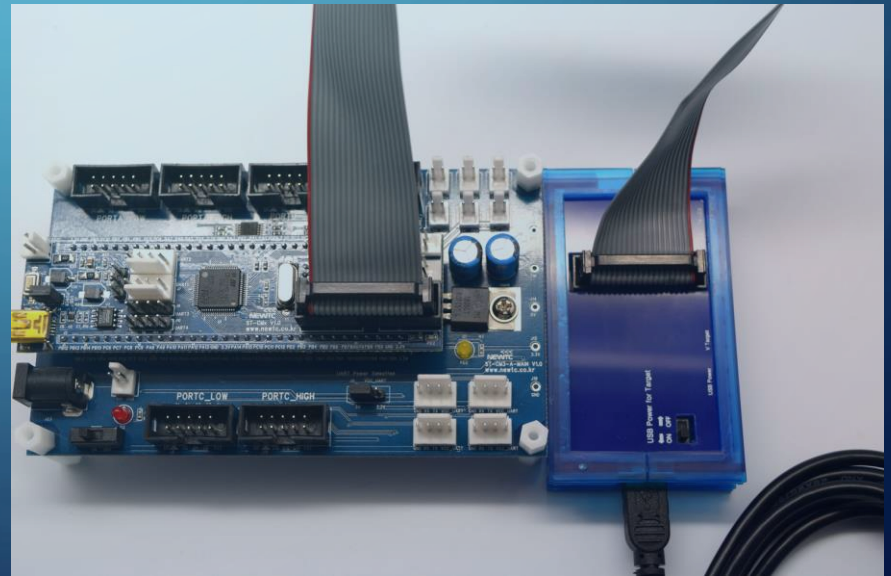


STM32 CORTEX-M3 프로그램 다운로드하기

(주) 뉴티씨

대표이사 류대우





목차

1. OpenOCD 설정

2. 프로그램 다운로드

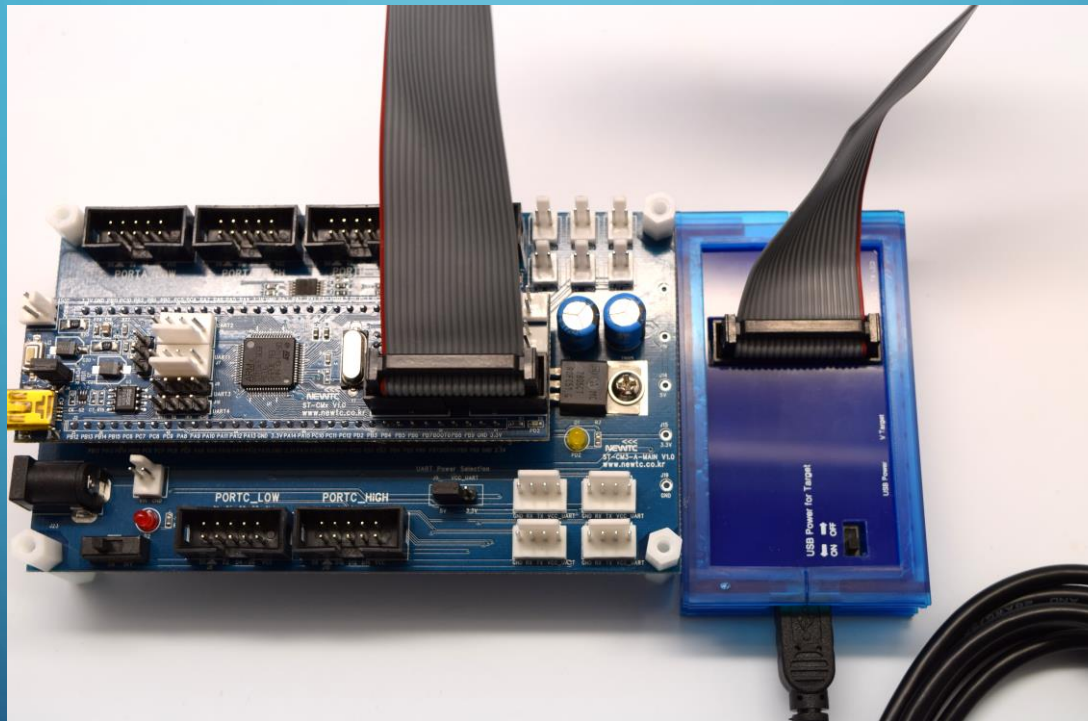
3. 프로세서 리셋



1. OPENOCD 설정 개요

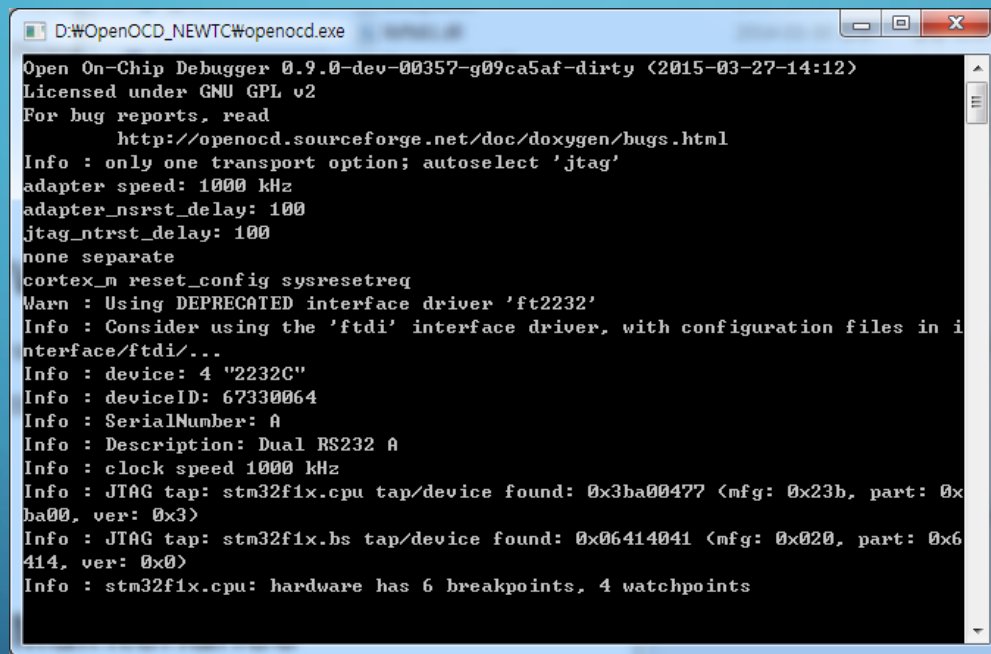
- 1) I-OPENDBG-ARM을 ARM 보드에 연결한다
- 2) 자료실에서 내려받은 OpenOCD 실행 파일을 실행한다.
- 3) Eclipse 에서 디버깅을 설정한다.

1-(1). OPENOCD 설정



I-OPENDBG-ARM 을 ST-CM3-A-MAIN 보드와 사진처럼 연결

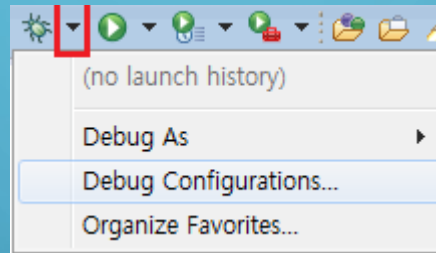
1-(2). OPENOCD 설정



```
D:\OpenOCD_NEWTC\openocd.exe
Open On-Chip Debugger 0.9.0-dev-00357-g09ca5af-dirty (2015-03-27-14:12)
Licensed under GNU GPL v2
For bug reports, read
    http://openocd.sourceforge.net/doc/doxygen/bugs.html
Info : only one transport option; autoselect 'jtag'
adapter speed: 1000 kHz
adapter_nsrst_delay: 100
jtag_ntrst_delay: 100
none separate
cortex_m reset_config sysresetreq
Warn : Using DEPRECATED interface driver 'ft232r'
Info : Consider using the 'ftdi' interface driver, with configuration files in i
interface/ftdi/...
Info : device: 4 "2232C"
Info : deviceID: 67330064
Info : SerialNumber: A
Info : Description: Dual RS232 A
Info : clock speed 1000 kHz
Info : JTAG tap: stm32f1x.cpu tap/device found: 0x3ba00477 (mfg: 0x23b, part: 0x
ba00, ver: 0x3)
Info : JTAG tap: stm32f1x.bs tap/device found: 0x06414041 (mfg: 0x020, part: 0x6
414, ver: 0x0)
Info : stm32f1x.cpu: hardware has 6 breakpoints, 4 watchpoints
```

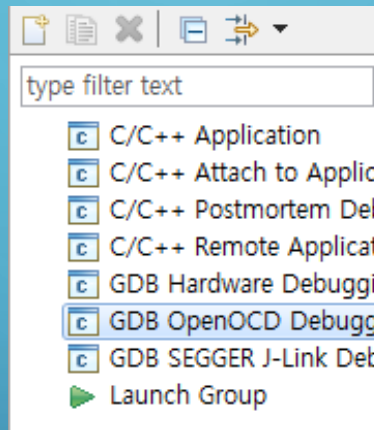
openocd.exe를 실행. 상단 사진처럼 나타나면 성공

1-(3)-A. OPENOCD 설정-ECLIPSE



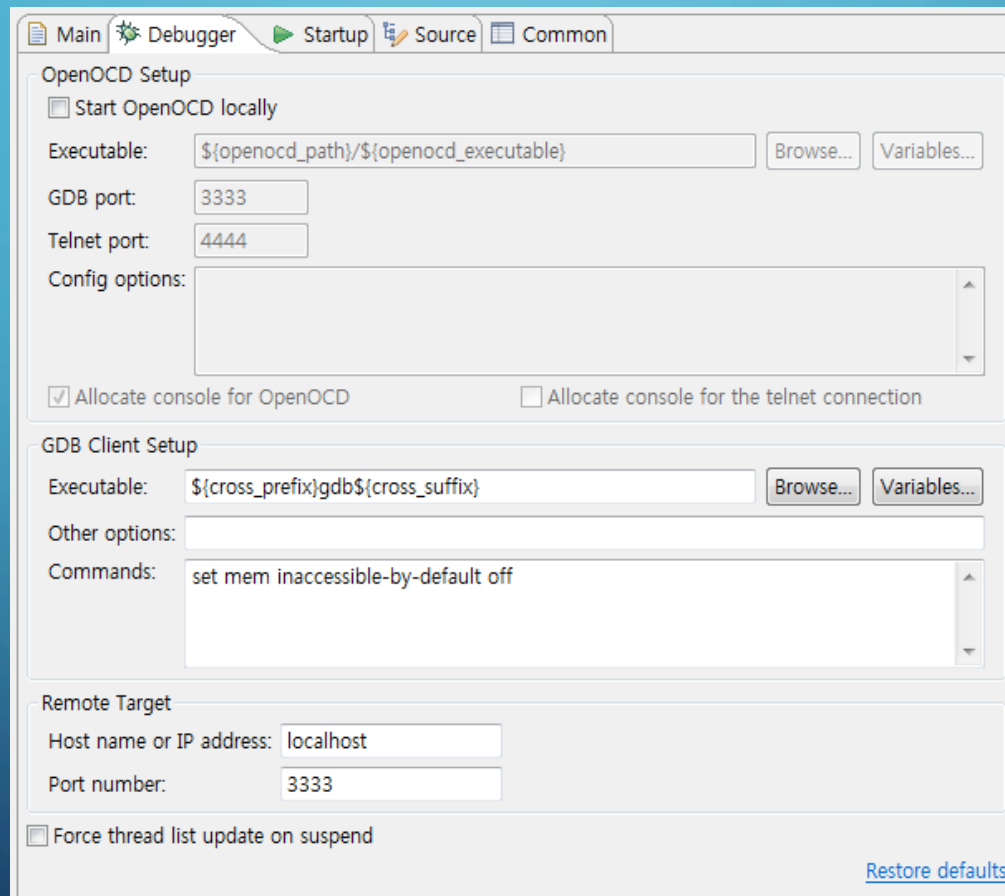
Debug Configurations를 선택하여 디버그 구성 열기

1-(3)-B. OPENOCD 설정-ECLIPSE



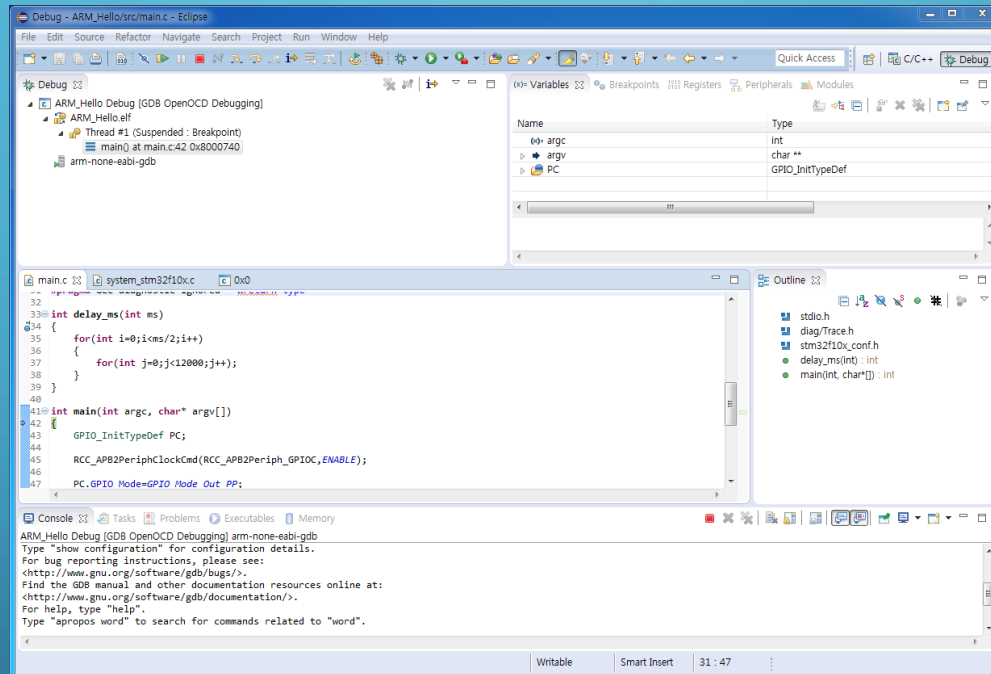
GDB OpenOCD Debugging 더블 클릭

1-(3)-C. OPENOCD 설정-ECLIPSE



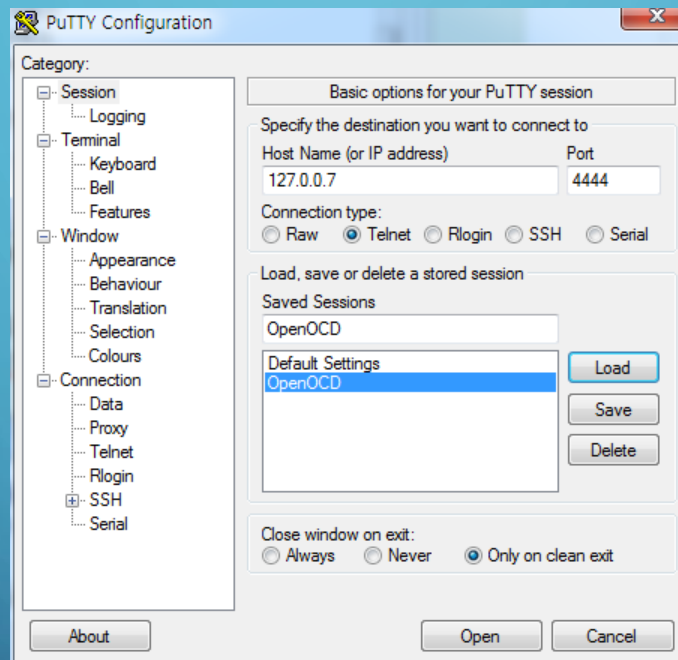
다음과 같이 디버그 구성 수정 후 Debug 버튼 클릭

1-(3)-D. OPENOCD 설정-ECLIPSE



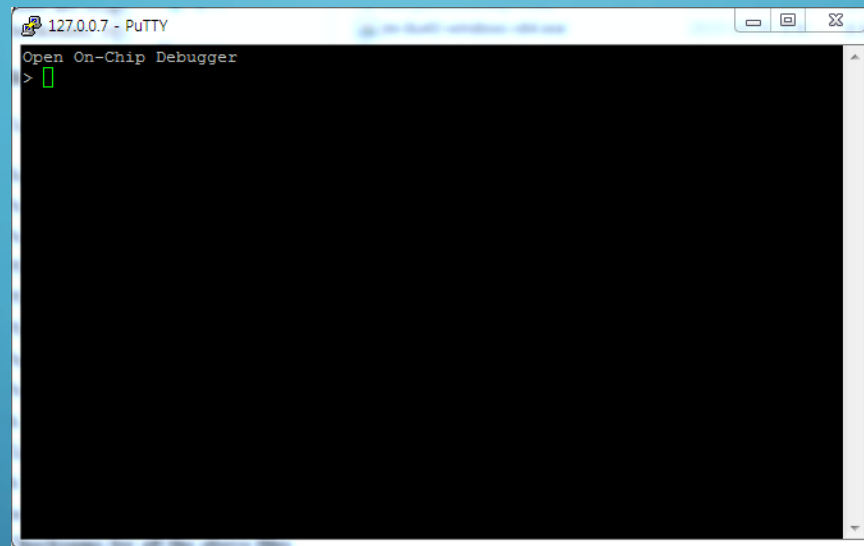
IDE 변경 확인. 변경이 되었다면 다운로드 성공.

2- (1). MCU 리셋 설정



PuTTY를 실행하고 호스트 이름 및 포트를 다음과 같이 설정
그 후 Open 버튼 클릭

2- (2). MCU 리셋 설정



다음과 같은 창이 나타나면 다음 명령을 입력 :

reset run